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Electronic Version v1.1

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**Title of
Invention****METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE
CURRENT IN CMOS INTEGRATED CIRCUIT**

Application Number :

Date :

First Named Applicant: Brent A. Anderson

Confirmation Number:

Attorney Docket Number: BUR920040019US1

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Documents being submitted:	Files
us-assignment	BUR920040019US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040019Assign1.tif BUR920040019Assign2.tif
us-request	BUR920040019US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040019US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920040019US1-usidst.xml us-ids.dtd us-ids.xsl
us-declaration	BUR920040019Dec1.tif
application-body	BUR920040019-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920040019drw1.tif BUR920040019drw2.tif BUR920040019drw3.tif BUR920040019drw4.tif BUR920040019drw5.tif BUR920040019drw6.tif

Comments
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